

QFS000001000000

MSA and TAA 10GBase-Converter QSFP+ Transceiver (QSFP+ to SFP+ Converter)

Product Description

This MSA Compliant QSFP+ to SFP+ converter provides conversion from QSFP+ to SFP+ form factors. It is built to MSA standards and is uniquely serialized and data-traffic and application tested to ensure that they will integrate into your network seamlessly. Digital optical monitoring (DOM) support is also present to allow access to real-time operating parameters. This converter is Trade Agreements Act (TAA) compliant. We stand behind the quality of our products and proudly offer a limited lifetime warranty.

Skylane's transceivers are RoHS compliant and lead-free.

Features:

- Compliant to industry standards
- SFP+ MSA SFF-8431
- QSFP MSA SFF-8436
- Matched impedance
- Secure latching mechanism
- Low insertion loss
- RoHS-6 compliant
- Built-in EEPROM for product identification



Applications:

- 40GBase Ethernet
- Access and Enterprise

For your product safety, please read the following information carefully before any manipulation of the transceiver:



This transceiver is specified as ESD threshold 1kV for SFI pins and 2kV for all others electrical input pins, tested per MIL-STD-883G, Method 3015.4 /JESD22-A114-A (HBM). However, normal ESD precautions are still required during the handling of this module.



LASER SAFETY

This is a Class1 Laser Product according to IEC 60825-1:2007. This product complies with 21 CFR 1040.10 and 1040.11 except for deviations pursuant to Laser Notice No. 50, dated (June 24, 2007).

The optical ports of the module need to be terminated with an optical connector or with a dust plug in order to avoid contamination.

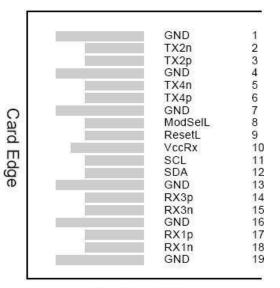
Recommended Operating Conditions

| Parameter | Symbol | Min | Max | Unit |
|------------------------------------|--------|------|------|------|
| Operating Case Temperature | Тс | -20 | 85 | °C |
| Storage Temperature | Tstg | -40 | 85 | °C |
| Relative Humidity (Non-Condensing) | RH | | 85 | % |
| Supply Voltage | Vcc3 | 3.15 | 3.45 | V |
| Power Consumption | POUT | | 0.3 | W |
| Characteristic Impedance | lm | 90 | 110 | Ω |

QSFP Host Board Connector Pin-Out

| 38 | GND | Till the state of |
|----|---------|---|
| 37 | TX1n | |
| 36 | TX1p | |
| 35 | GND | |
| 34 | TX3n | |
| 33 | TX3p | |
| 32 | GND | |
| 31 | LPMode | 5 |
| 30 | Vcc1 | |
| 29 | VccTx | |
| 28 | IntL | |
| 27 | ModPrsL | |
| 26 | GND | |
| 25 | RX4p | |
| 24 | RX4n | |
| 23 | GND | |
| 22 | RX2p | |
| 21 | RX2n | |
| 20 | GND | |

Top Side Viewed from Top



Bottom Side Viewed from Bottom

Pin Definitions

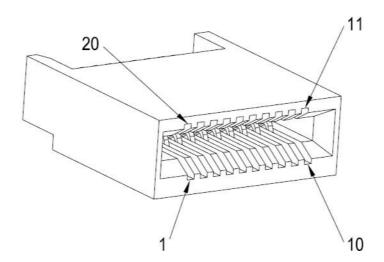
| Pin | Logic | Symbol | Name/Description | Note |
|-----|------------|---------|---------------------------------------|------|
| 1 | | GND | Module Ground. | 1 |
| 2 | CML-I | Tx2- | Transmitter Inverted Data Input. | |
| 3 | CML-I | Tx2+ | Transmitter Non-Inverted Data Output. | |
| 4 | | GND | Module Ground. | 1 |
| 5 | CML-I | Tx4- | Transmitter Inverted Data Input. | |
| 6 | CML-I | Tx4+ | Transmitter Non-Inverted Data Output. | |
| 7 | | GND | Module Ground. | 1 |
| 8 | LVTLL-I | ModSelL | Module Select. | |
| 9 | LVTLL-I | ResetL | Module Reset. | |
| 10 | | VccRx | +3.3V Power Supply Receiver. | 2 |
| 11 | LVCMOS-I/O | SCL | 2-Wire Serial Interface Clock. | |
| 12 | LVCMOS-I/O | SDA | 2-Wire Serial Interface Data. | |
| 13 | | GND | Module Ground. | |
| 14 | CML-O | Rx3+ | Receiver Non-Inverted Data Output. | |
| 15 | CML-O | Rx3- | Receiver Inverted Data Output. | |
| 16 | | GND | Module Ground. | 1 |
| 17 | CML-O | Rx1+ | Receiver Non-Inverted Data Output. | |
| 18 | CML-O | Rx1- | Receiver Inverted Data Output. | |
| 19 | | GND | Module Ground. | 1 |
| 20 | | GND | Module Ground. | 1 |
| 21 | CML-O | Rx2- | Receiver Inverted Data Output. | |
| 22 | CML-O | Rx2+ | Receiver Non-Inverted Data Output. | |
| 23 | | GND | Module Ground. | 1 |
| 24 | CML-O | Rx4- | Receiver Inverted Data Output. | 1 |
| 25 | CML-O | Rx4+ | Receiver Non-Inverted Data Output. | |
| 26 | | GND | Module Ground. | 1 |
| 27 | LVTTL-O | ModPrsL | Module Present. | |
| 28 | LVTTL-O | IntL | Interrupt. | |
| 29 | | VccTx | +3.3V Power Supply Transmitter. | 2 |
| 30 | | Vcc1 | +3.3V Power Supply. | 2 |
| 31 | LVTTL-I | LPMode | Low-Power Mode. | |
| 32 | | GND | Module Ground. | 1 |
| 33 | CML-I | Tx3+ | Transmitter Non-Inverted Data Input. | |
| 34 | CML-I | Tx3- | Transmitter Inverted Data Output. | |

| 35 | | GND | Module Ground. | 1 |
|----|-------|------|--------------------------------------|---|
| 36 | CML-I | Tx1+ | Transmitter Non-Inverted Data Input. | |
| 37 | CML-I | Tx1- | Transmitter Inverted Data Output. | |
| 38 | | GND | Module Ground. | 1 |

Notes:

- 1. GND is the symbol for signal and supply (power) common for QSFP modules. All are common within the QSFP module, and all module voltages are referenced to this potential otherwise noted. Connect these directly to the host board signal common ground plane.
- 2. VccRx, Vcc1, and VccTx are the receiver and transmitter power suppliers and shall be applied concurrently. Recommended host board power supply filtering is shown below. VccRx, Vcc1, and VccTx may be internally connected within the QSFP transceiver module in any combination.

SFP+ Host Board Connector Pinout for SFP+



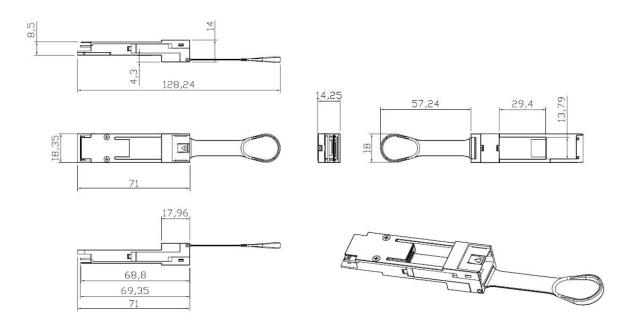
Pin Definitions

| Pin | Logic | Symbol | Name/Description | Note |
|-----|-----------|------------|---|------|
| 1 | | VeeT | Module Transmitter Ground. | 1 |
| 2 | LVTTL-O | Tx_Fault | Transmitter Fault. | 2 |
| 3 | LVTTL-I | Tx_Disable | Transmitter Disable. | 3 |
| 4 | LVTTL-I/O | SDA | MOD-DEF2 - 2-Wire Serial Interface Data. | 4 |
| 5 | LVTTL-I/O | SCL | MOD-DEF1 - 2-Wire Serial Interface Clock. | 4 |
| 6 | | MOD_ABS | Module Absent. | 5 |
| 7 | LVTTL-I | RS0 | Rate Select Zero. | |
| 8 | LVTTL- O | Rx_LOS | Module Receiver Loss of Signal. | 2 |
| 9 | LVTTL-I | RS1 | Rate Select One. | |
| 10 | | VeeR | Module Receiver Ground. | 1 |
| 11 | | VeeR | Module Receiver Ground. | 1 |
| 12 | CML-O | RD- | Receiver Inverted Data Output. | |
| 13 | CML-O | RD+ | Receiver Non-Inverted Data Output. | |
| 14 | | VeeR | Module Receiver Ground. | 1 |
| 15 | | VccR | Module Receiver 3.3V Supply. | |
| 16 | | VccT | Module Transmitter 3.3V Supply. | |
| 17 | | VeeT | Module Transmitter Ground. | 1 |
| 18 | CML-I | TD+ | Transmitter Non-Inverted Data Input. | |
| 19 | CML-I | TD- | Transmitter Inverted Data Input. | |
| 20 | | VeeT | Module Transmitter Ground. | 1 |

Notes:

- 1. The module signal grounds, VeeR and VeeT, shall be isolated from the module case.
- 2. This is an open collector/drain output and shall be pulled up with $4.7k\Omega-10k\Omega$ to Host_Vcc on the host board. Pull-ups can be connected to multiple power supplies; however, the host board design shall ensure that no module has voltage exceeding module VccT/R+0.5V.
- 3. This is an open collector/drain input and shall be pulled up with $4.7k\Omega-10k\Omega$ to VccT in the module.
- 4. See 2-wire electrical specifications.
- 5. This shall be pulled up with $4.7k\Omega-10k\Omega$ to Host_Vcc on the host board.

Mechanical Dimensions



Physical Specifications

| Parameter | Specification |
|--------------------------------|--------------------------|
| Maximum Dimensions (H x W x D) | 13.5mm x 18.4mm x 78mm |
| Weight | Typically less than 100g |

About Skylane Optics

Skylane is a leading provider of transceivers for optical communication.

We offer an extensive portfolio for the enterprise, access, datacenter and metropolitan fiber optical market as well as for smart home applications and home networks.

We cover the European, South American and North American market with a strong partner network and have offices in Belgium, Brazil, Sweden and USA.

Our offerings are characterized by high quality and performance. In combination with our strong technical support, we enable our customers to build cost optimized network solutions.

We offer an extensive range of high-quality products including transceivers (Optical and copper), Active Optical Cable (AOC), Direct Attach Cable (DAC), Mux/Demux, Coding Box (SKYGATE).











